

**AMENDMENTS TO THE SPECIFICATION**

Please amend the paragraph beginning at page 5, line 28 as follows:

--If the impedance control signal input to the impedance control terminal ~~103~~ 103a is a low logic level, on the other hand, the L-transmission circuit 104 closes the transmission gate 112a. The output voltage of the prebuffer 106 is thereby blocked. At the same time, the clamp Nch transistor 114a of the L-transmission circuit 104 is turned on to fix the gate electrode of the Nch transistor 111a of the L-main buffer 102 to a ground potential. The Nch transistor 111a is thereby turned off not to be selected as a drive transistor.--